

Applicants: Walter Fix et al.
Serial No.: 10/562,869
Filed: April 7, 2006
For: Logic Gate with a Potential-Free Gate Electrode for Organic Integrated Circuits
Examiner: Eva Y. Montalvo Art Unit: 2814
Attorney Dkt: 411000-144 Customer No. 27162

RESPONSE UNDER 37 CFR 1.112

MS Amendment
Commissioner for Patents
Box 1450
Alexandria, VA 22313-1450

This paper is in response to the Office Action dated June 24, 2009 having a response due date set to expire on Sept. 24, 2009. The Commissioner is authorized to charge deposit account 03 0678 for any fees that might be due for this paper or credit any over payments.

IN THE SPECIFICATION

Amend the specification as shown starting on page 2 of this paper.

IN THE CLAIMS

Amend the claims as shown starting on page 4 of this paper.

Remarks begin on page 6.